

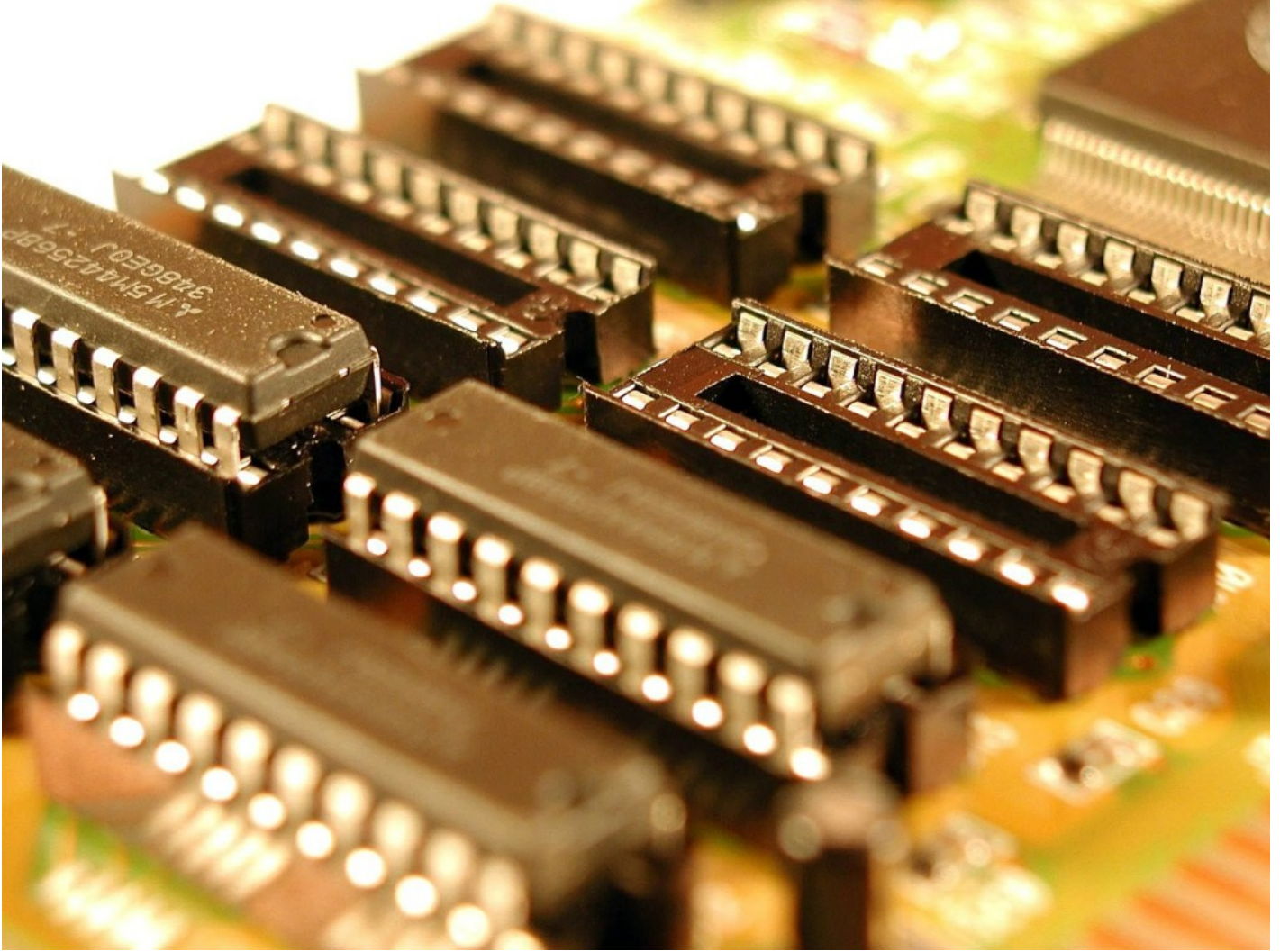


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<i>NUMBER</i>	<i>Function</i>
74HC00	Quad 2-input NAND gate
74HC02	Quad 2-input NOR gate
74HC04	Hex inverter(buffered)
74HC08	Quad 2-input AND gate
74HC14	Hex inverting Schmitt trigger
74HC20	Dual 4-input NAND gate
74HC30	8-input NAND gate
74HC32	Quad 2-input OR gate
74HC42	BCD-to-decimal decoder
74HC74	Dual D flip-flop with preset and clear
74HC85	4-bit magnitude comparator
74HC123	Dual monostable multivibrator
74HC132	Quad 2-input NAND Schmitt trigger
74HC138	3-to 8- line decoder
74HC139	Expandable dual 2- to 4- line decoder
74HC154	4- to 16- line decoder (use 24LSP socket)
74HC161	Synchronous Binary Counter
74HC163	Synchronous Binary Counter
74HC164	8-bit serial in parallel out shift register
74HC165	8-bit parallel in serial out shift register
74HC174	Hex D flip-flop with clear
74HC175	Quad D type Flip-Flop with clear
74HC191	Up-down binary counter
74HC192	Synchronous decade up-down counter

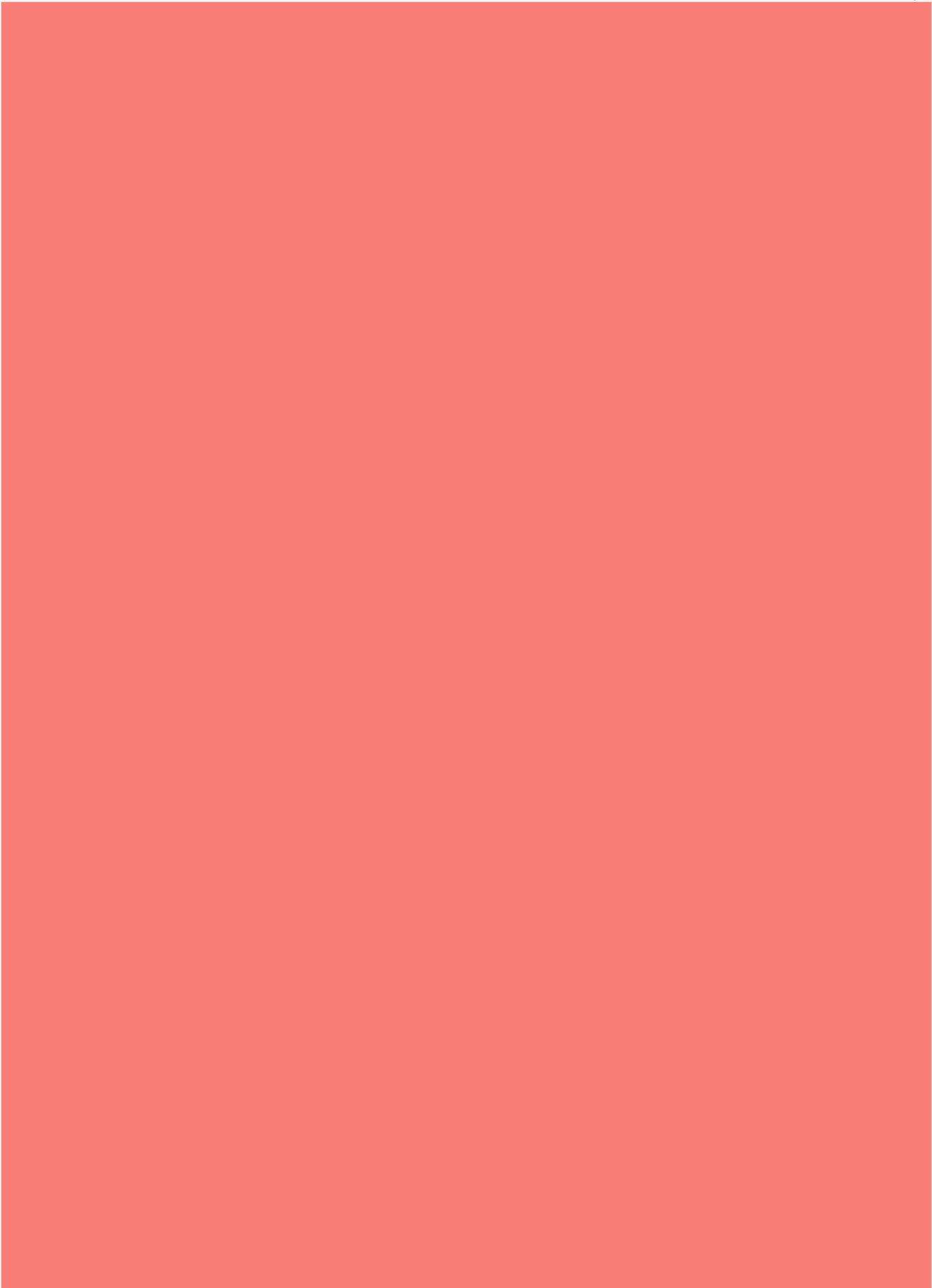
74HC193	Synchronous binary up-down counter
74HC221	Dual monostable multivibrator
74HC240	Inverting octal tri-state buffer
74HC244	Octal tri-state buffer
74HC245	Octal tri-state transceiver
74HC257	Quad 2-channel tri-state multiplexer
74HC273	Octal D flip-flop
74HC367	Tri-state hex buffer
74HC373	Tri-state octal D-type latch
74HC374	Tri-state octal D type Flip-Flop
74HC390	Dual 4-bit decade counter
74HC393	Dual 4-bit binary counter
74HC541	Octal buffer-line driver (tri-state)
74HC573	Tri-state octal D type latch
74HC574	Tri-state octal D-type Flip-Flop
74HC595	8-bit serial-to-parallel shift register latch
74HC688	8-bit magnitude comparator (equality detector)
74HC942	Full Duplex low-speed
74HC943	Full duplex 300-baud modem chip
74HC4017	Decade counter-divider with 10 decoded outputs
74HC4020	14-stage binary counter
74HC4040	12-stage binary counter
74HC4046	CMOS phase-lock loop
74HC4060	Quad analog switch-baud modem chip
74HC4514	4- to 16- line decoder with latch (use 24LSP socket)
74HC4538	Dual retriggerable monostable multivibrator

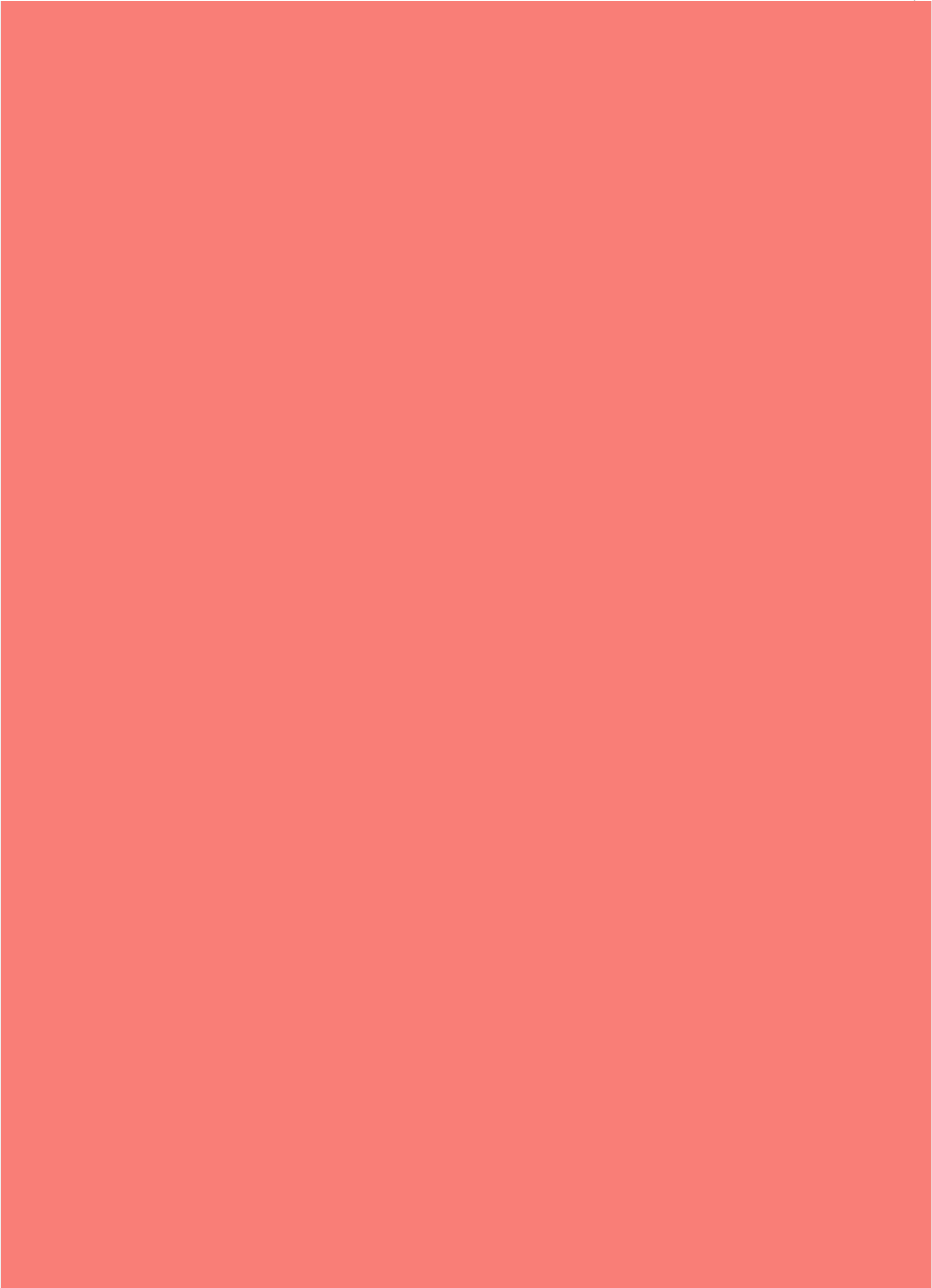


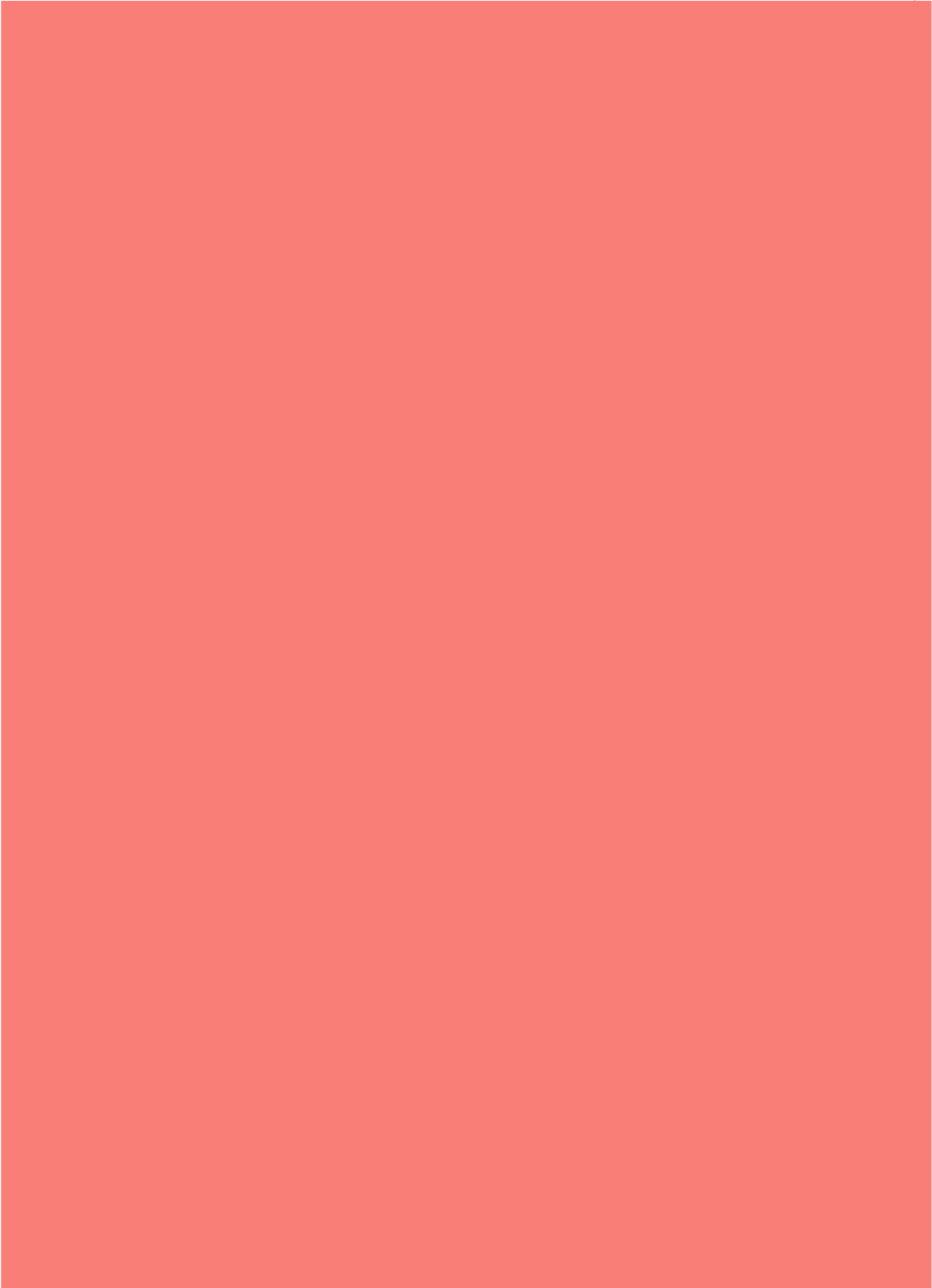
Manas Sharma

I'm a physicist specializing in computational material science with a PhD in Physics from Friedrich-Schiller University Jena, Germany. I write efficient codes for simulating light-matter interactions at atomic scales. I like to develop Physics, DFT, and Machine Learning related apps and software from time to time. Can code in most of the popular languages. I like to share my knowledge in Physics and applications using this Blog and a YouTube channel.

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